

<b>Notice of References Cited</b>	Application/Control No. 09/410,928	Applicant(s)/Patent Under Reexamination JONES ET AL.	
	Examiner Brian R. Peugh	Art Unit 2187	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2002/0007442	01-2002	FARRALL et al.	711/134
	B	US-5,432,918	07-1995	Stamm, Rebecca L.	711/156
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chu et al., "Write Buffer Design for On-Chip Cache", © 1994, IEEE< p. 311-316.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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